



UNIVERSITÀ DI PAVIA

Anno Accademico 2018/2019

RF MICROELECTRONICS

Anno immatricolazione	2018/2019
Anno offerta	2018/2019
Normativa	DM270
SSD	ING-INF/01 (ELETTRONICA)
Dipartimento	DIPARTIMENTO DI INGEGNERIA INDUSTRIALE E DELL'INFORMAZIONE
Corso di studio	ELECTRONIC ENGINEERING
Curriculum	Microelectronics
Anno di corso	1°
Periodo didattico	Primo Semestre (01/10/2018 - 18/01/2019)
Crediti	9
Ore	90 ore di attività frontale
Lingua insegnamento	English
Tipo esame	SCRITTO E ORALE CONGIUNTI
Docente	SVELTO FRANCESCO (titolare) - 9 CFU
Prerequisiti	Basic knowledge of Analog Electronics
Obiettivi formativi	<p>This course is aimed at introducing students to the design of integrated transceivers for wireless communications. At the end, attendees will have gained knowledge of fundamental parameters describing system performances, insights into alternative processing architectures and ability to design the following building blocks: low noise amplifiers, up and down converters, power amplifier, phase locked loop, phase detector, filter, voltage controlled oscillator, frequency synthesizer. As an example, the design of a complete transceiver will be described starting from the specifications of a telecommunication application, such as GSM, W-CDMA, WLAN. Based on computer aided design experience carried out in the Laboratory, the student will be able to finalize the design of single blocks integrated in most advanced CMOS nodes and will be ready for the design of complete transceivers.</p>

<p>Programma e contenuti</p>	<p>Basic concepts for transceivers Non-linearities, inter-symbol interference, phase noise in oscillators, sensitivity and dynamic range, image rejection.</p> <p>Modulation Choice of digital schemes for optimum power and spectral efficiency in wireless systems.</p> <p>Transceiver architectures Super-heterodyne direct conversion, low-IF, Direct Conversion.</p> <p>Design of CMOS integrated blocks low-noise amplifier; up and down-converters; voltage controlled oscillators; phase detector; charge pump, filters, phase locked loop; frequency synthesizer; power amplifiers.</p> <p>Application example Design of CMOS transceiver building blocks for applications of commercial interest.</p>
<p>Metodi didattici</p>	<p>Lectures (hours/year in lecture theatre): 90 Practical class (hours/year in lecture theatre): 0 Practicals / Workshops (hours/year in lecture theatre): 0</p>
<p>Testi di riferimento</p>	<p>B. Razavi. RF Microelectronic circuits. Prentice Hall PTR, Upper Saddle River, NJ 07458.</p>
<p>Modalità verifica apprendimento</p>	<p>At the end of the course, the student will prepare a report regarding the design carried out in the Laboratory. The examination includes a set of exercises to be solved by the student, followed by an oral discussion on the course program. No intermediate examination is foreseen.</p>
<p>Altre informazioni</p>	<p>At the end of the course, the student will prepare a report regarding the design carried out in the Laboratory. The examination includes a set of exercises to be solved by the student, followed by an oral discussion on the course program. No intermediate examination is foreseen.</p>
<p>Obiettivi Agenda 2030 per lo sviluppo sostenibile</p>	<p>Sbl legenda sviluppo sostenibile</p>