



UNIVERSITÀ DI PAVIA

Anno Accademico 2015/2016

INDUSTRIAL INFORMATICS AND EMBEDDED SYSTEMS

Anno immatricolazione	2015/2016
Anno offerta	2015/2016
Normativa	DM270
SSD	ING-INF/05 (SISTEMI DI ELABORAZIONE DELLE INFORMAZIONI)
Dipartimento	DIPARTIMENTO DI INGEGNERIA INDUSTRIALE E DELL'INFORMAZIONE
Corso di studio	INGEGNERIA ELETTRICA
Curriculum	SISTEMI ELETTRICI
Anno di corso	1°
Periodo didattico	Secondo Semestre (29/02/2016 - 10/06/2016)
Crediti	6
Ore	45 ore di attività frontale
Lingua insegnamento	ENGLISH
Tipo esame	SCRITTO E ORALE CONGIUNTI
Docente	LEPORATI FRANCESCO (titolare) - 6 CFU
Prerequisiti	Basic knowledge of electronics, industrial electronics, computer architecture and computer fundamentals.
Obiettivi formativi	The aim of the course is to provide competences and skills about informatics methodologies targeting industrial processes development and management. At the end of the course, students will be able to deal with computer interfacing with control systems, sensor data acquisition, driving typical industrial actuators, digital data communication and elaboration signal filtering and processing. The course aims, moreover, at introducing students to embedded systems, by offering an overview of typical hw/sw programmable architectures.
Programma e contenuti	Data acquisition Digital interfacing and signal edge/level detection; COTS components driving (latch, buffer, counters). Electric and algorithmic filtering. Pulse

signals acquisition: C source codes. Optical encoders: C source codes for velocity and position detection. Pulse signal output, numerical codes acquisition from contraves and absolute encoders. Multiplexing and ADC. Sigma/Delta converters. Linearization, calibration ARMA numerical filters, exponential, moving average filters.

Motor drivers

Hw-sw interface for DC motors and servomotors. Pulse Width Modulation and H-bridge devices.

Digital communication

Serial and parallel communications: bit, characters, message synchronization. Source code examples concerning IEEE GPIB 488, RS232, RS485 buses. Drivers for USART devices and point to point serial communication code example.

Numerical filters

Low-pass, High-pass, Band-pass, IIR and FIR filters, autoregressive moving-average (ARMA).

Field bus

Communication networks based on field buses. The Fip and Can protocols.

Embedded systems

ARM 7 processor architecture. Instruction set, memory access, source code assembly examples. FPGA: history, technology, use. Laboratory activities using Altera FPGAs and the Quartus II development environment.

Metodi didattici

Lectures (hours/year in lecture theatre): 45
Practical class (hours/year in lecture theatre): 0
Practicals / Workshops (hours/year in lecture theatre): 0

Testi di riferimento

Wayne Wolf. Computer as components. Morgan Kaufmann. Reference text for the embedded systems lectures.

Lorenzo Mezzalana. Dispense di Informatica Industriale - Handouts of Industrial Informatics. Reference for lectures on digital and analog interface, on digital communication and field buses (in Italian language)

Francesco Loporati. Lectures slides. . Slides of the lessons, available at <http://gamma.unipv.it/infind>.

Modalità verifica apprendimento

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Altre informazioni

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Obiettivi Agenda 2030 per lo sviluppo sostenibile

[\\$Ibl legenda sviluppo sostenibile](#)